



Patent
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Tatsuo Sengoku et al.
Application No.: 10/730,123
Filed: December 9, 2003
For: SEMICONDUCTOR INTEGRATED
CIRCUIT WITH SIMPLY
CONFIGURED TEMPERATURE
DETECTION CIRCUIT

) Mail Stop AF
)) Reply Under 37 C.F.R. 1.116
)) Expedited Procedure
)) Technology Center 2800
)) Group Art Unit: 2859
)) Examiner: Stanley J. Pruchnic
)) Confirmation No.: 4345

RESPONSE UNDER 37 C.F.R. §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: March 7, 2006

Sir:

This Response responds to the Office Action dated December 9, 2005, (Paper No. 20051206).

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